
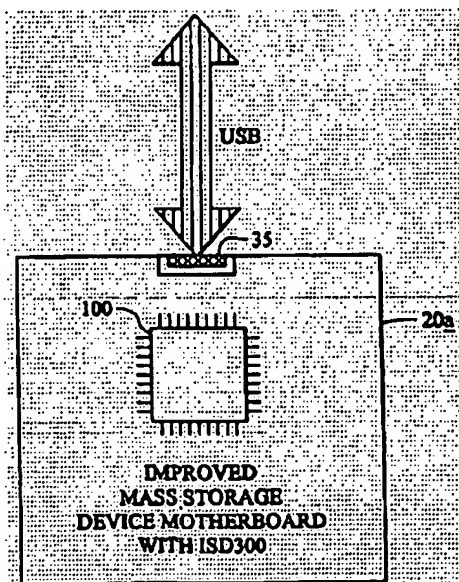


UNIVERSAL SERIAL BUS (USB) INTERFACE FOR MASS STORAGE DEVICE**Publication number:** WO0241154.**Publication date:** 2002-05-23**Inventor:** HARRIS DAVID H (US); CLARK GORDON R (US);
HOLLAND STEPHEN D (US)**Applicant:** CYPRESS SEMICONDUCTOR CORP (US); HARRIS
DAVID H (US); CLARK GORDON R (US); HOLLAND
STEPHEN D (US)**Classification:****- International:** G06F13/36; G06F3/06; G06F13/00; G06F13/38;
G06F13/40; G06F13/36; G06F3/06; G06F13/00;
G06F13/38; G06F13/40; (IPC1-7): G06F13/00**- European:** G06F3/06D; G06F13/40D5**Application number:** WO2001US43945 20011116**Priority number(s):** US20000249530P 20001117.**Also published as:** WO0241154 (A3)**Cited documents:** EP0890905 US6199122 EP1111498 EP0987876[Report a data error here](#)**Abstract of WO0241154**

A mass storage device motherboard or secondary board includes a bridging circuit. The bridging circuit converts signals from the mass storage device into USB signals. The bridging circuit can be provided by a chip that converts ATA/ATAPI signals into USB signals.



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